

**IN THE CLAIMS:**

1. (currently amended)      An oscillator circuit comprising a resonance circuit formed of a resonator as an inductor component and dividing capacitors, each dividing capacitors having a first end connected to the resonator and a second end connected to ground, an oscillation amplifier driven by a power voltage connected to said resonance circuit, and a pull-down resistor provided between an output terminal of said oscillation amplifier and ground, wherein said pull-down resistor ~~aets as~~ is serially-connected dividing resistors, the serially-connected dividing resistors being a first pull-down resistor connected circuit side and a second pull-down resistor connected ground side, and also a bias capacitor is directly connected ~~provided~~ between a connection point between said dividing resistors and ground.

2. (original) The oscillator circuit according to claim 1, wherein said oscillation amplifier is configured of an emitter coupled logic (ECL) circuit.

3. (original) The oscillator circuit according to claim 1, wherein said oscillator is a crystal oscillator in which a crystal is used in the resonator thereof.

4. (currently amended) A frequency-switching oscillator comprising: wherein a two input, two output type of oscillation amplifier having signals of mutually opposite phase is connected to a resonance circuit formed of a resonator and dividing capacitors, the dividing capacitors are connected to each end, respectively, of the resonator as well as each dividing capacitor being connected to ground; a first resonance circuit provided with a first electronic switch is connected between a pair of input-output terminals for signals of mutually opposite phase; and a second resonance circuit provided with a second electronic switch is connected between another pair of input-output terminals; wherein the resonance frequencies of said first and second resonance circuits are different and also said first and second electronic switches are selectively switched to select one of said resonance circuits and further comprising a bias resistor connected to each input of the oscillation amplifier.

5. (original) The oscillator according to claim 4, wherein said oscillation amplifier is configured of an emitter coupled logic (ECL) circuit.

6. (original) The oscillator according to claim 4, wherein said oscillator is a voltage controlled oscillator such that said dividing capacitor acts as a variable-voltage capacitance element and a control voltage is applied thereto to vary the oscillation frequency.

7. (original) The oscillator according to claim 4, wherein said oscillator is a crystal oscillator in which a quartz crystal is used in the resonator thereof.

8. (original) The oscillator according to claim 4, wherein said oscillator is a ceramic oscillator in which a ceramic is used in the resonator thereof.

Please add the following new claims.

9.(new) The oscillator circuit according to claim 1, wherein the oscillation amplifier includes a differential amplifier having two inputs and two outputs of mutually opposite phases.

10. (new) The oscillator circuit according to claim 1, wherein a resistance of the first pull-down resistor is less than a resistance of the second pull-down resistor.

11. (new) The oscillator circuit according to claim 1, wherein the resistance of the first pull-down resistor is  $10\Omega$  and the resistance of the second pull-down resistor is  $150\Omega$ .

12. (new) The oscillator circuit according to claim 1, further comprising a resistor connected in series with the inductor component for reducing high-frequency current flowing therethrough.

13. (new) The oscillator according to claim 4, further comprising a feedback resistor connected in parallel with each resonator.

14. (new) The oscillator according to claim 4, wherein the first resonance circuit is of the 150-MHz band and the second resonance circuit is of the 600-MHz band.